


Name of Teaching Faculty	Dr. Nilesh Bhaskarrao Bahadure				
Designation	Assistant Professor				
Department	Electronics & Telecommunication Engineering				
Date of Joining the Institution	3rd July 2017				
Qualification with Class/ Grade/Year	UG	PG	Ph.D.	Others	
	BE/I/2000	ME/I/2005	2017	---	
Total Experience in Years	Teaching	Industry	Research	Admin	
	15 Yr 1 Month	5 Years	4 Years	7 Years	
No. of Paper Published	Journals		Conference		
	National	International	National	International	
	06	11	06	04	
PG Teacher/Ph.D. Guide Approval	PG Approval No. / Date No. PG/2/of 2005 dated 1 st July, 2005			Ph.D. Approval No. / Date --	
	Subject	Area	University	Subject	University
	Electronics	Electronics	CSVTU Bhilai	--	--
Ph.D. / ME Students Guided	Ph.D.		M.E/ M.Tech		
	Completed	In-process	Completed	In-process	
	00	00	08	00	
No. of Books Authored/ Reviewed/ IPRs / Patents	Authored	Reviewed	IPRs	Patents	
	01	01	00	00	
Conferences, Workshop, QIP and Seminars Attended in Various Capacities	<ul style="list-style-type: none"> • Convener/Member of Organizing Committee: 12 • Chairperson of Technical Session: 01 • Resource Person: 01 • Participation as Delegate: 04 • Presented Paper/s: 09 				
Professional Membership & Designation	<ul style="list-style-type: none"> • Life Member of Institute of Engineers (India) • Life Member of ISTE • Life Member of IETE • Life Member of SESI • Life Member of ISCA • Life Member of IAENG • Life Member of ISRS 				

Consultancy Activities	•
Achievements & Awards	<ol style="list-style-type: none"> 1. Reviewer for the Springer - Journal of Medical and Biological Engineering and British Journal of Mathematics & Computer Science 2. Reviewer and Technical Program Committee member for the IEEE International Conference on Intelligent Communication and Computational Techniques (ICCT' 2017) at Manipal University Jaipur 3. Signed Institute MOU with National Institute of Electronics and Information Technology - NIELIT (An Autonomous Scientific Society of Department of Electronics and Information Technology, Ministry of Communications & Information Technology, Government of India) to start All India PG Diploma Programs in VLSI & Embedded Systems at Bhilai Institute of Technology Raipur. 4. Signed Institute MOU with “National Institute of Solar Energy – NISE (An Autonomous Institute of Ministry of New & Renewable Energy, Government of India), New Delhi” for training courses, PG diploma courses, training programs and other professional courses on renewable energies at Bhilai Institute of Technology Raipur. 5. Member of the Editorial - Board of International Journal of Electronic Design Engineering (MAT Journals http://matjournals.in/index.php/JOEDE/about/editorialTeam) 6. Member of the Editorial - Board of International Journal of Advanced Engineering and Innovative Technology (http://ijaieit.com/Editorial- Board.html) 7. Selected by Royal University of Bhutan in year 2007, for the Grade B Lecturer (Under Civil Services of Bhutan) in the Department of Electrical Engineering, Royal Bhutan Institute of Technology, Phuentsholing, Bhutan.
Grants Fetched	<p>➤ Minor Research Grant: Received Project Grant of Rs. 0.50 Lakhs from The Institution of Engineers (India) for the UG Project on “Sign Language Detection using Kinect Sensor in Matlab”, Duration 6 months from July 2016 to December</p>
Interaction with Professional Institutions/Colleges	--