


Name of Teaching Faculty	Prof. Akhil Saya				
Designation	Assistant Professor				
Department	Electrical Engineering				
Date of Joining the Institution	10 th July 2017				
Qualification with Class/ Grade/Year	UG	PG	Ph.D.	Others	
	BE/I/2012	M.Tech/I/2014	--	--	
Total Experience in Years	Teaching	Industry	Research	Admin	
	1 Yrs	2 Years	--	--	
No. of Paper Published	Journals		Conference		
	National	International	National	International	
	00	00	00	00	
PG Teacher/Ph.D. Guide Approval	PG Approval No. / Date			Ph.D. Approval No. / Date	
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	Subject	Area	University	Subject	University
Electrical Engineering	Power Electronics	VIT Vellore	--	--	
Ph.D. / ME Students Guided	Ph.D.		PG (ME/M.Tech, MSc)		
	Completed	In-process	Completed	In-process	
	00	00	00	00	
No. of Books Authored/ Reviewed/ IPRs / Patents	Authored	Reviewed	IPRs	Patents	
	00	00	00	00	
Conferences, Workshop, QIP and Seminars Attended in Various Capacities					
Professional Membership & Designation					
Consultancy Activities					
Achievements & Awards	<ol style="list-style-type: none"> 1. Secured 1st position in paper presentation competition organized by department of electrical engineering in PVG COE, Pune. 2. Participated in paper presentation organized by VJTI, Mumbai which was a part of industry institution interactive program by Schneider Electric. 3. Certification of profession in Foundation level in Software Testing. 				

	<ol style="list-style-type: none">4. Attended a workshop on PLC and SCADA using Allen Bradley PLC.5. Secured second position for technical paper in Science, Engineering and Technology Conference, 2013 at VIT University, Vellore
Grants Fetched	--
Interaction with Professional Institutions/Colleges	--